Patent

Attorney Docket No.: Intel 2207/1012302

Assignee: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Sailesh KOTTAPALLI et. al.

SERIAL NO. OF

PARENT APPLICATION : 09/753,060

FILING DATE OF

PARENT APPLICATION : December 29, 2000

FOR : METHOD AND APPARATUS FOR IMPROVING

DISPERSAL PERFORMANCE IN A PROCESSOR

THROUGH THE USE OF NO-OP PORTS

ART UNIT OF

PARENT APPLICATION : 2183

EXAMINER OF

PARENT APPLICATION : William M. TREAT

M/S: PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail No. EV351181305US in an envelope addressed to: M/S: PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450,

Alexandria, VA 22313-1450 on

Date: March 19, 2004

Barbara Vance

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Submitted herewith is an Information Disclosure Statement under 37 CFR §1.97(b)(3) with three (3) reference(s) listed on the attached PTO-1449 (modified) form. Unless otherwise indicated herein, one copy of each reference is attached.

It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear

among the "References Cited" on any patent to issue therefrom.

If any fee is required, the Office is hereby authorized to charge any fees, or credit any overpayments under 37 CFR §1.17(p) to Kenyon & Kenyon, Deposit Account No. 11-0600.

Respectfully submitted,

KENYON & KENYON

Dated: March 19, 2004

Stephen T. Neal (Reg. No. 47,815)

Attorneys for Intel Corporation

KENYON & KENYON 333 West San Carlos St., Suite 600 San Jose, CA 95110

Telephone:

(408) 975-7500

Facsimile:

(408) 975-7501

Attorney Docket No. Serial No. Intel 2207/1012302 **TBA** LIST OF PATENTS AND PUBLICATIONS **Applicant** Sailesh KOTTAPALLI et al. **Group Art Unit Filing Date**

TBA

U. S. PATENT DOCUMENTS

Herewith

Form PTO-1449 (modified)

FOR APPLICANT(S)' INFORMATION

DISCLOSURE STATEMENT

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
	6,195,756	February 27, 2001	Hurd	713	340	December 15, 1998
	6,442,701	August 27, 2002	Hurd	713	324	October 8, 1999
	6,564,316	May 13, 2003	Perets et al.	712	219	September 9, 1999

FOREIGN PATENT DOCUMENTS

						TRANSLA	ATION
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO

OTHER DOCUMENTS

AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.